

08-01-05

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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants: SOUK, Jun-Hyung; LEE, Jeong-Young; YOON, Jong-Soo; CHOI, Kwon-Young; and BAEK, Bum-Ki  
Assignee: Samsung Electronics Co., Ltd.  
Title: THIN FILM TRANSISTOR ARRAY PANEL AND MANUFACTURING METHOD THEREOF  
Application No.: 10/759,389 Filing Date: 01/16/2004  
Examiner: Ton, Minh Toan T. Group Art Unit: 2871  
Docket No.: AB-1351 US Confirmation No.: 6707

San Jose, California  
July 29, 2005

Mail Stop Amendment  
COMMISSIONER FOR PATENTS  
P.O. Box 1450  
Alexandria, VA 22313-1450

**RESPONSE TO RESTRICTION REQUIREMENT**

Dear Sir:

In response to the Restriction Requirement having a mailing date of June 29, 2005, Applicants hereby elect the claims of Group I (Claims 1-5), drawn to a thin film transistor array, classified in class 349, subclass 43, for further prosecution. Applicants reserve the right to prosecute the non-elected claims of Group II (Claims 6-14) in a divisional application.

The Examiner is invited to call the undersigned at (408) 392-9250 with any questions regarding the above-identified application.

EXPRESS MAIL LABEL NO.

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Respectfully submitted,

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